

REMARKS

The present amendment is responsive to the Office Action issued February 21, 2008. Claims 1, 2, and 7 have been canceled. Claims 19, 25, 27, 41, and 51 have been amended. No new matter has been added. Accordingly, claims 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 are presently pending.

Claim Objections

Claims 1, 2, 19, 20, 35, 37, 45, and 48 were objected to for informalities. Claims 1 and 2 have been canceled, thereby obviating their rejections.

Claims 19, 35, and 37 have been amended in accordance with the Examiner's suggestions. Claims 20, 45, and 48 were objected to for reciting "a predetermined data amount" as opposed to "the predetermined data amount." Applicants respectfully submit that the present form of the claims is appropriate, because the predetermined data amount referred to could in fact be a different predetermined data amount than recited in claims 19, 35, and 37. Accordingly, Applicants request that the rejections of claims 19-20, 35, 37, 45, and 48 be withdrawn.

35 U.S.C. § 112 Rejections

Claims 1-2, 7, 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 are rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement. Specifically, the Examiner asserts that the recited "control means" and "index adding means, residing outside a central processing unit" suggests the control means and central processing unit to be different entities. Moreover, the Examiner asserts that support for such interpretation does not exist.

Applicants respectfully disagree with the Examiner's interpretation of the claim. The location of the index adding means does not suggest any type of relationship between control means and the CPU. Nevertheless, even accepting the Examiner's interpretation, the statement that the claim is not enabled is plainly false. For example, referring to Fig. 2, the hard disk control unit 14 is clearly separate from the CPU 1.

Accordingly, Applicants respectfully submit that claims 1-2, 7, 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 are properly enabled under §112, first paragraph, and request that the rejections of these claims be withdrawn.

The rejections of claims 1, 2, and 7 are obviated by the cancellation of these claims.

Claims 1-2, 7, 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 are rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement. The Examiner appears to merely couple this rejection along with the enablement rejection discussed above. Therefore, for at least the reasons discussed above in connection with the enablement rejection, Applicants respectfully submit that pending claims 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 properly accord with 35 U.S.C. § 112, first paragraph, and request that the rejections be withdrawn.

Claims 35, 45-46 were further rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement. Particularly, the Examiner separately asserts that the specification is enabling for "using a memory control means to control writing of said extracted packet," but is not enabling for "using a memory control means to control writing of said stored packet." Applicants are confused by this rejection, because claim 35 does in fact

recite "using a memory control means to control writing of said extracted packet." Because the Examiner indicated in the rejection that such a limitation was properly enabled, Applicants request that the rejection of claims 35 and 45-46 be withdrawn.

Claims 1-2, 7, 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Particularly, the Examiner requires Applicants to identify the elements associated with each "means." Accordingly, Applicants have provided the following chart, including examples of elements identified in the specification that may be associated with the corresponding means. It is noted that the examples merely illustrate embodiments of the invention that are within the scope of the claims. Accordingly, the claims are not limited to covering the embodiments set forth in the examples.

Means	Exemplary elements
Receiving means	Antenna (11), receiver (22), and associated elements (See, e.g., Figs. 1 and 2).
Extracting means	PID parser (51)
Memory means	SDRAM (60), register, buffer (102, 105, 106, 107), hard disk drive (15)
Memory control means	Hard disk control unit (14, 50), SDRAM controller (59), FIFO controller (63), DMA controller (68), hard disk controller (41), control machine (131-134), controller (28)

Index adding means	Index adding unit (64)
Packets transferring control means	PIO state machine (84), DMA state machine (86), IDE state machine (87), host command buffer (102), control machine (131-134),
Updating means	LBA determining unit 104 (LBA determining circuit 136)

35 U.S.C. § 103 Rejections

Claims 1-2, 7, 19-20, 35, 37, 41, 43, 45-46, 48-49, and 51 are rejected under 35 U.S.C. § 103(a) as being patentable over Applicant Admitted Prior Art ("AAPA") in view of U.S. Patent No. 5,881,248 to Mergard et al. ("Mergard").

Claims 1, 2, and 7 have been canceled, thereby obviating their rejections.

Claim 19 has been amended to recite:

A digital broadcast receiving apparatus communicating with a hard disk drive, comprising:

...
 packets transferring control means for permitting write access of said packets outputted from index adding means to said hard disk drive. . .

updating means for updating said set address information for DMA transfer;

wherein said index adding means adds the address including at least one of an address in said hard disk drive in which a just-previous block has been recorded, an address in said hard disk drive in which a current block is recorded, and an address in said hard disk drive in which a just-subsequent block is recorded to said block; and

wherein said updating means has an internal counter for automatically setting said address information.

Neither AAPA nor Mergard disclose any updating means, let alone an updating means capable of automatically setting address information. Further, neither AAPA nor Mergard discloses index adding means adding an address in the hard disk drive.

For at least the reasons discussed above, Applicants respectfully submit that neither AAPA nor Mergard, taken alone or in combination, teach all the limitations of claim 19. Accordingly, Applicants respectfully request that the rejection of claim 19 be withdrawn.

Claims 35 and 37 were rejected on the same grounds as claim 19, and have been amended to include similar limitations to claim 19. Therefore, for at least the reasons discussed above in connection with claim 19, Applicants respectfully request that the rejections of claims 35 and 37 be withdrawn.

Claims 20, 41, 43, 45-46, 48-49, and 51 each depend from one of independent claims 19, 35, and 37, and thus include all the limitations thereof. Therefore, for at least the reasons discussed in connection with claims 19, 35, and 37, Applicants respectfully submit that claims 20, 41, 43, 45-46, 48-49, and 51 are patentable over the cited art. Accordingly, Applicants request that the rejections of claims 20, 41, 43, 45-46, 48-49, and 51 be withdrawn.

As it is believed that all of the rejections set forth in the Official Action have been fully met, favorable reconsideration and allowance are earnestly solicited. If, however, for any reason the Examiner does not believe that such action can be taken at this time, it is respectfully requested that he/she telephone applicant's attorney at (908) 654-5000 in order to overcome any additional objections which he might have.

Application No.: 09/913,688

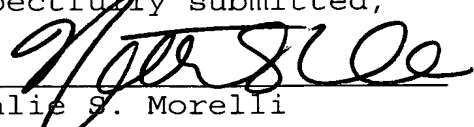
Docket No.: SONYSU 3.3-135

If there are any additional charges in connection with this requested amendment, the Examiner is authorized to charge Deposit Account No. 12-1095 therefor.

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Respectfully submitted,

By



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